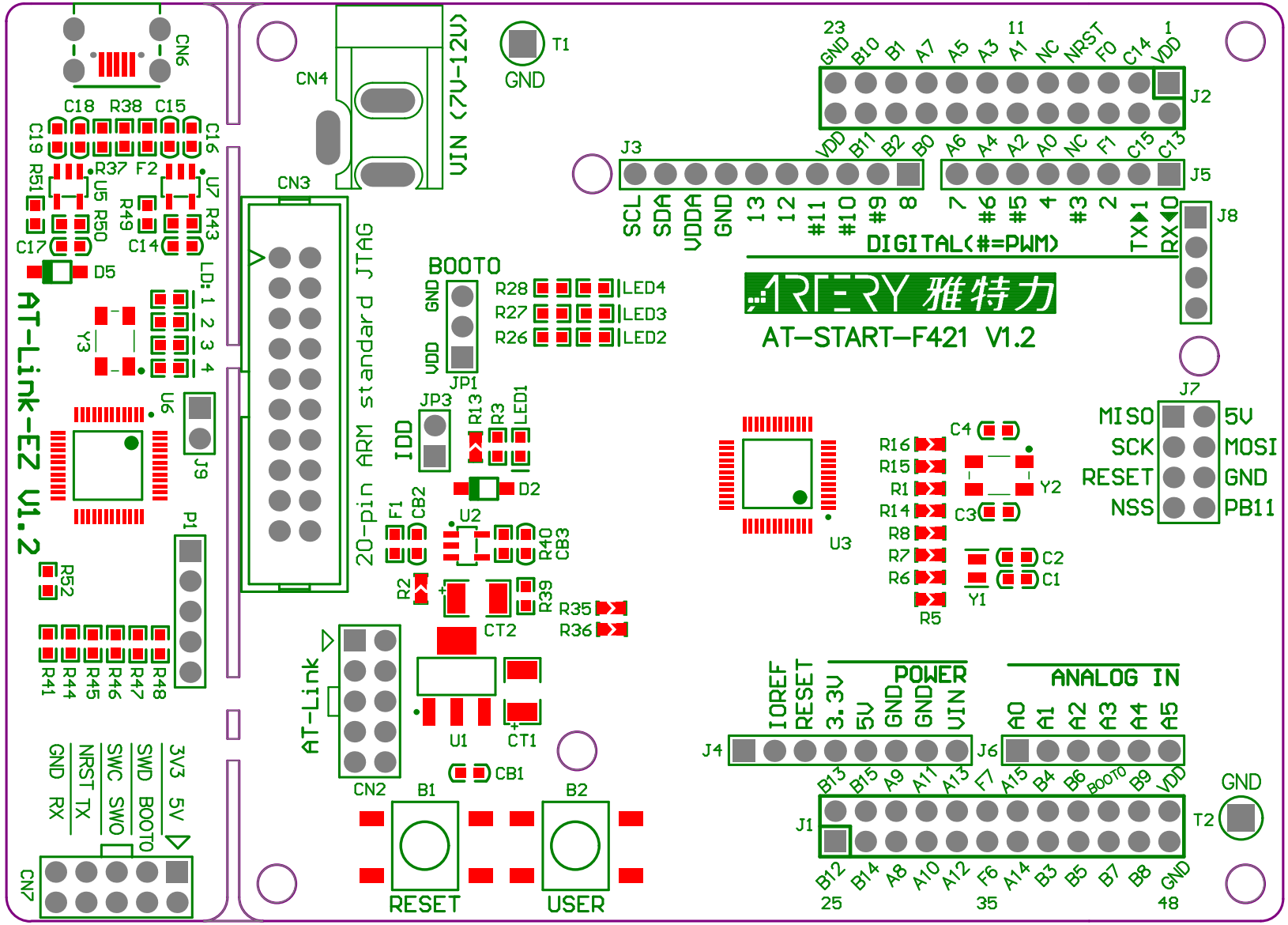
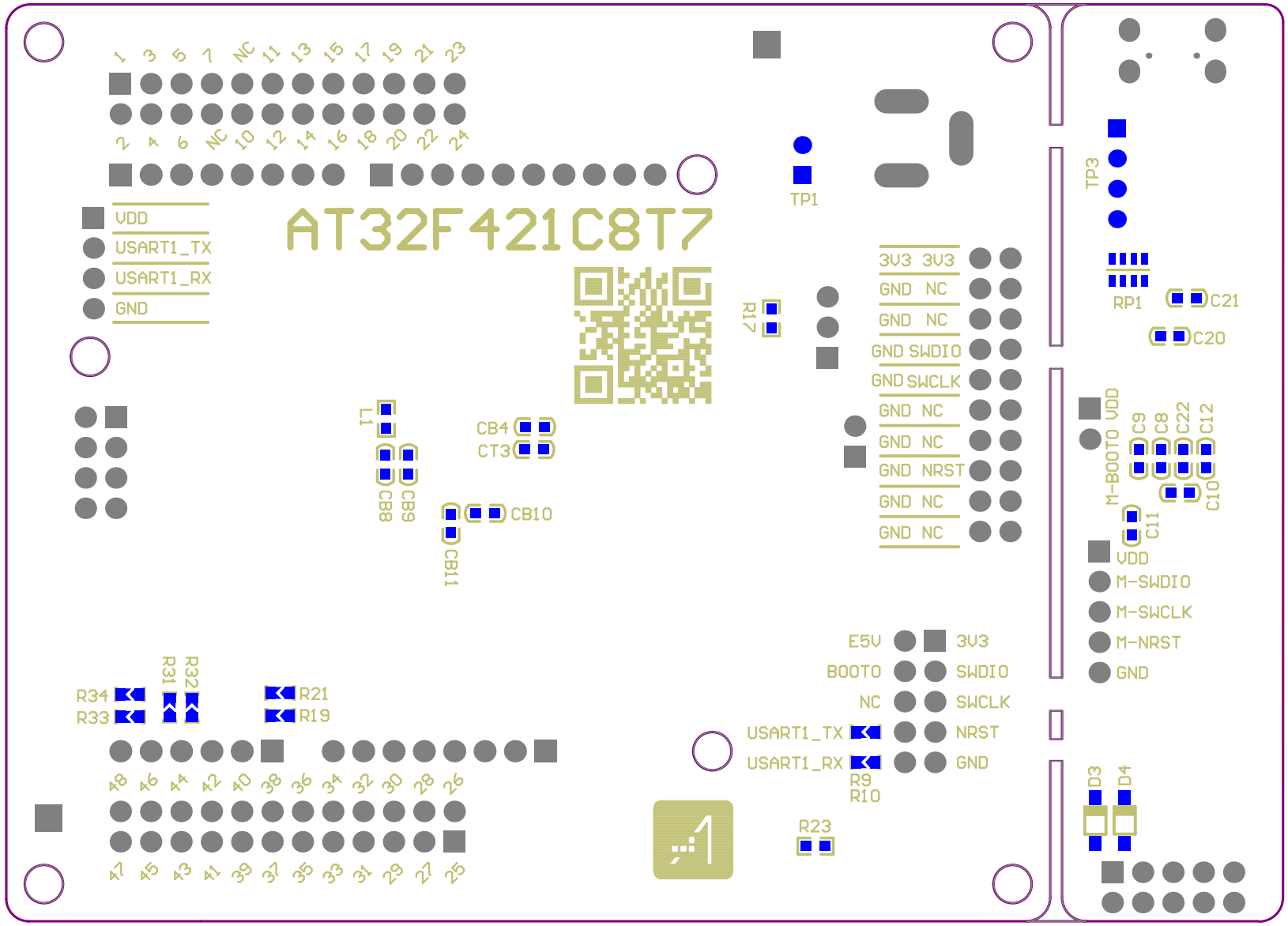

AT-START-F421 V1.2
ARTERY 雅特力
DIGITAL(#=PWM)
SCL SDA VDDA GND 13 12 #11 #10 #9 8
7 #6 #5 4 #3 2 TX▶1 RX◀0
VDD B11 B2 B0
A6 A4 A2 A0 NC F1 C15 C13
GND B10 B1 A7 A5 A3 A1 NC NRST F0 C14 VDD
23 11 1
J3 J5 J2 J8
T1 GND
VIN (7V-12V)
CN4
CN3
20-pin ARM standard JTAG
BOOT0
GND VDD
JP1
R28 R27 R26
LED4 LED3 LED2
JP3
IDD
R13 R3 LED1
D2
F1 CB2
U2
R40 CB3
R2
CT2
R39
R35 R36
U1
CT1
CB1
B1 B2
RESET USER
AT-Link
CN2
U3
R16 R15 R1 R14 R8 R7 R6 R5
C4 Y2 C3 C2 C1 Y1
MISO SCK RESET NSS
5V MOSI GND PB11
J7
POWER
IOREF RESET 3.3V 5V GND GND VIN
J4
ANALOG IN
A0 A1 A2 A3 A4 A5
J6
B13 B15 A9 A11 A13 F7 A15 B4 B6 BOOT0 B9 VDD
J1
B12 B14 A8 A10 A12 F6 A14 B3 B5 B7 B8 GND
25 35 48
T2 GND
CN6
C18 R38 C15
C19 C16
R37 F2
R51 U5 R50 U7 R43
R49
C17 C14
D5
LD: 1 2 3 4
Y3
U6
J9
P1
R52
R41 R44 R45 R46 R47 R48
AT-Link-EZ V1.2
3V3 5V
SWD BOOT0
SWC SWO
NRST TX
GND RX
CN7


AT32F421C8T7
VDD
USART1_TX
USART1_RX
GND
3V3 3V3
GND NC
GND NC
GND SWDIO
GND SWCLK
GND NC
GND NC
GND NRST
GND NC
GND NC
E5V
BOOT0
NC
USART1_TX
USART1_RX
3V3
SWDIO
SWCLK
NRST
GND
M-BOOT0 VDD
VDD
M-SWDIO
M-SWCLK
M-NRST
GND
TP1
TP3
RP1
C21
C20
R17
L1
CB4
CT3
CB8
CB9
CB10
CB11
C9
C8
C22
C12
C10
C11
R34
R33
R31
R32
R21
R19
R9
R10
R23
D3
D4