

General Description

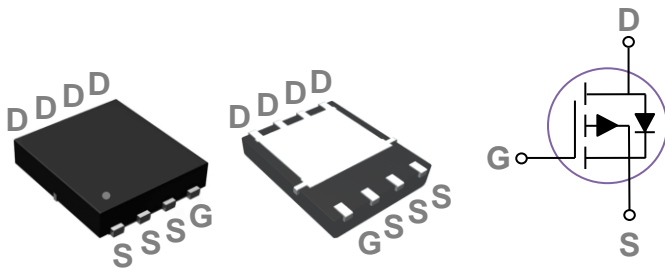
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	R _{DS(ON)}	I _D
-40V	15mΩ	-50A

Features

- -40V, -50A, R_{DS(ON)} = 15mΩ @ V_{GS} = -10V
- Improved dv/dt capability
- Fast switching
- Green Device Available

PPAK5X6 Pin Configuration



Applications

- Motor Drive
- Power Tools
- LED Lighting

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-40	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous (T _C =25°C)	-50	A
	Drain Current – Continuous (T _C =100°C)	-32	A
I _{DM}	Drain Current – Pulsed ¹	-200	A
EAS	Single Pulse Avalanche Energy ²	100	mJ
IAS	Single Pulse Avalanche Current ²	-45	A
P _D	Power Dissipation (T _C =25°C)	82	W
	Power Dissipation – Derate above 25°C	0.65	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to Ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	1.53	°C/W

Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-40	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-40V, V_{GS}=0V, T_J=25^\circ C$	---	---	-1	μA
		$V_{DS}=-32V, V_{GS}=0V, T_J=125^\circ C$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-15A$	---	12	15	m Ω
		$V_{GS}=-4.5V, I_D=-10A$	---	17	23	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.2	-1.6	-2.5	V
gfs	Forward Transconductance	$V_{DS}=-10V, I_D=-3A$	---	11	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3, 4}	$V_{DS}=-20V, V_{GS}=-10V, I_D=-25A$	---	37	56	nC
Q_{gs}	Gate-Source Charge ^{3, 4}		---	5	8	
Q_{gd}	Gate-Drain Charge ^{3, 4}		---	8.5	13	
$T_{d(on)}$	Turn-On Delay Time ^{3, 4}	$V_{DD}=-20V, V_{GS}=-10V, R_G=6\Omega, I_D=-25A$	---	9.6	15	ns
T_r	Rise Time ^{3, 4}		---	28	42	
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}		---	45	68	
T_f	Fall Time ^{3, 4}		---	11	17	
C_{iss}	Input Capacitance	$V_{DS}=-20V, V_{GS}=0V, F=1MHz$	---	2070	3100	pF
C_{oss}	Output Capacitance		---	225	340	
C_{rss}	Reverse Transfer Capacitance		---	170	255	
R_g	Gate resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	---	6.8	---	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V, \text{ Force Current}$	---	---	-50	A
I_{SM}	Pulsed Source Current		---	---	-100	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=-1A, T_J=25^\circ C$	---	---	-1	V
t_{rr}	Reverse Recovery Time ³	$V_R=-30V, I_S=-10A$	---	45	---	ns
Q_{rr}	Reverse Recovery Charge ³		$di/dt=100A/\mu s, T_J=25^\circ C$	---	25	---

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-45A, R_G=25\Omega, \text{ Starting } T_J=25^\circ C$
3. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

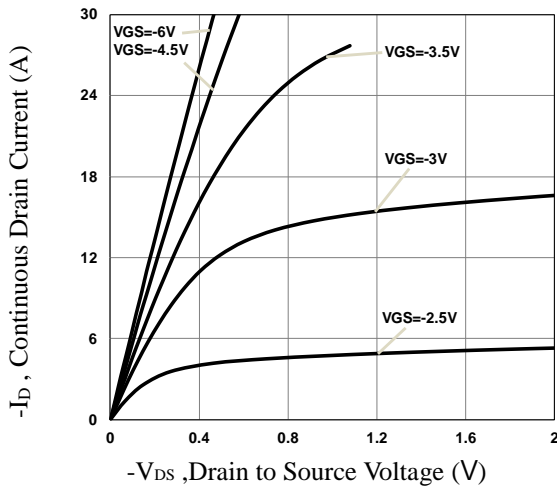


Fig.1 Typical Output Characteristics

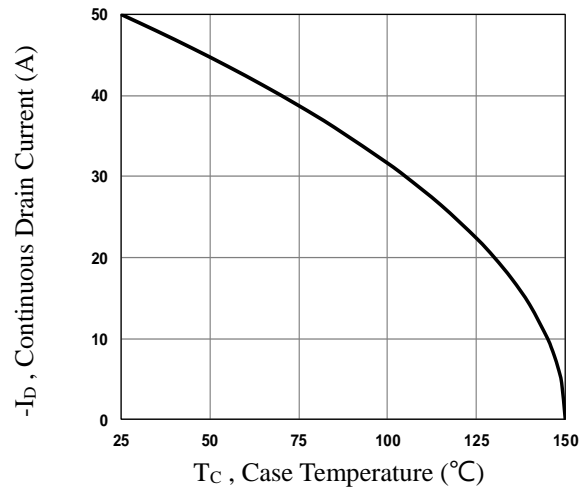


Fig.2 Continuous Drain Current vs. T_c

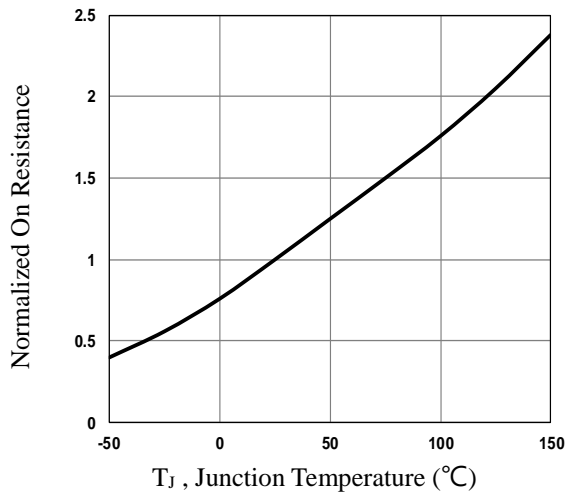


Fig.3 Normalized $R_{DS(on)}$ vs. T_J



Fig.4 Normalized V_{th} vs. T_J

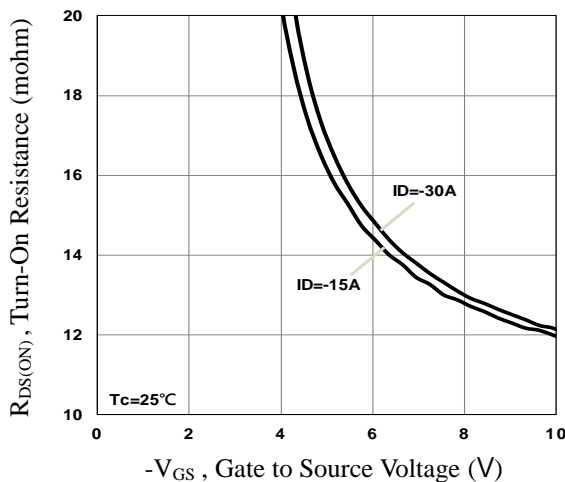


Fig.5 Turn-On Resistance vs. V_{GS}

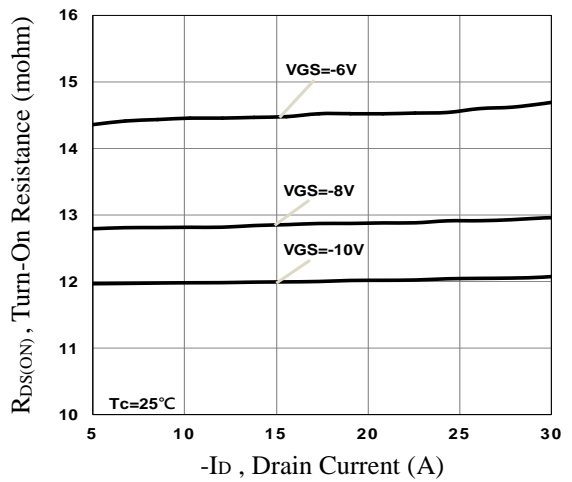


Fig.6 Turn-On Resistance vs. I_D

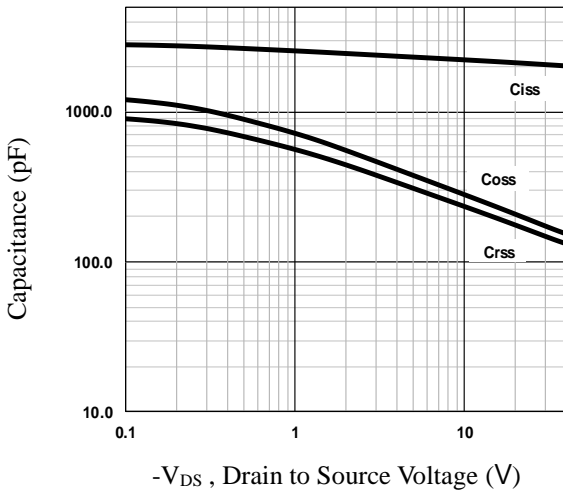


Fig.7 Capacitance Characteristics

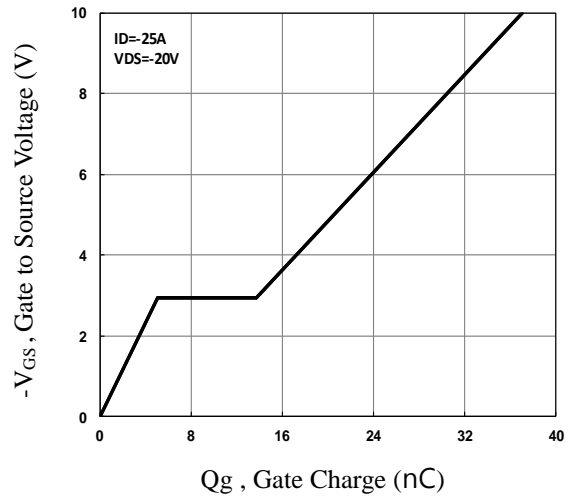


Fig.8 Gate Charge Characteristics

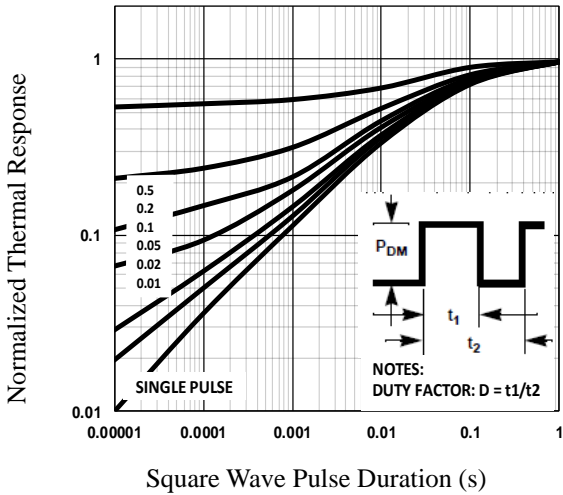


Fig.9 Normalized Transient Impedance

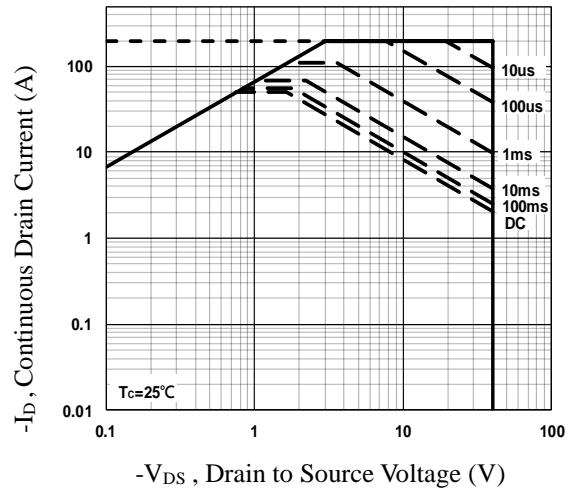


Fig.10 Maximum Safe Operation Area

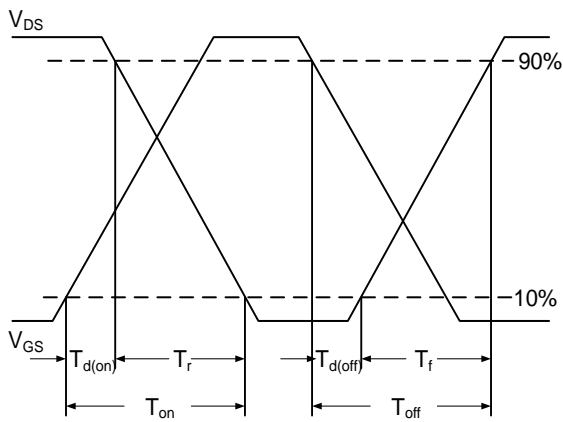


Fig.11 Switching Time Waveform

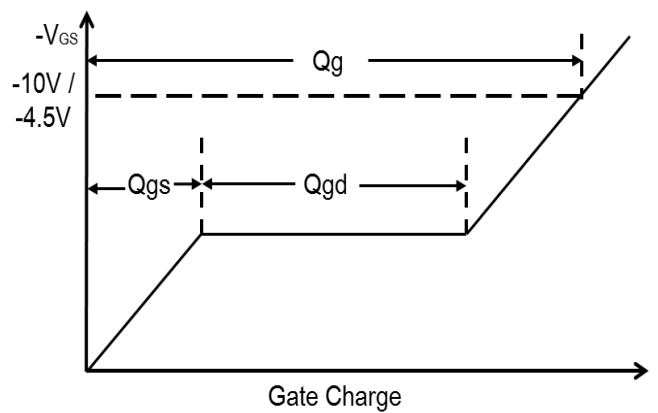
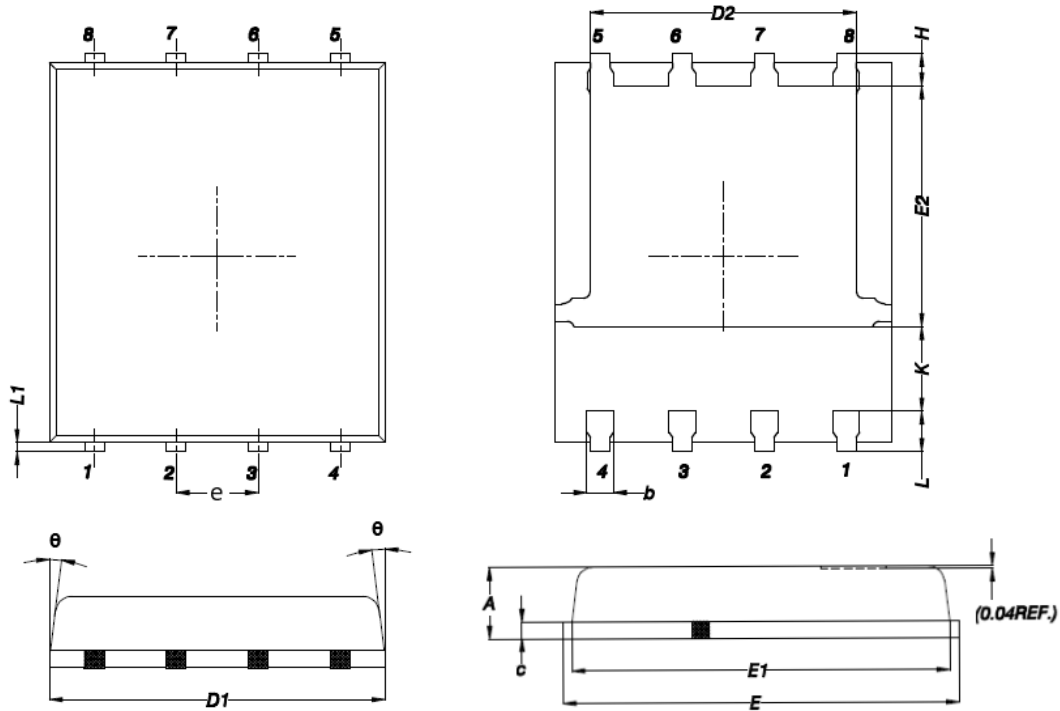


Fig.12 Gate Charge Waveform

PPAK5x6 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.200	0.850	0.047	0.031
b	0.510	0.300	0.020	0.012
C	0.300	0.200	0.012	0.008
D1	5.400	4.800	0.212	0.189
D2	4.310	3.610	0.170	0.142
E	6.300	5.850	0.248	0.230
E1	5.960	5.450	0.235	0.215
E2	3.920	3.300	0.154	0.130
e	1.27BSC		0.05BSC	
H	0.650	0.380	0.026	0.015
K	---	1.100	---	0.043
L	0.710	0.380	0.028	0.015
L1	0.250	0.050	0.009	0.002
θ	12°	0°	12°	0°