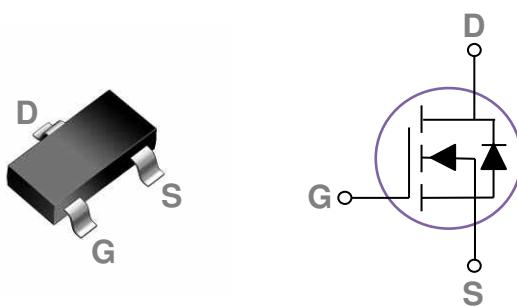


General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

SOT23-3S Pin Configuration



BVDSS	RDS(ON)	ID
60V	75mΩ	3.2A

Features

- 60V, 3.2A, $RDS(ON) = 75m\Omega @ VGS = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

Applications

- Motor Drive
- Power Tools
- LED Lighting

Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_c=25^\circ C$)	3.2	A
	Drain Current – Continuous ($T_c=100^\circ C$)	2	A
I_{DM}	Drain Current – Pulsed ¹	12.8	A
P_D	Power Dissipation ($T_c=25^\circ C$)	1.56	W
	Power Dissipation – Derate above 25°C	0.012	W/°C
T_{STG}	Storage Temperature Range	-50 to 150	°C
T_J	Operating Junction Temperature Range	-50 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	80	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	60	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=1\text{mA}$	---	0.05	---	$\text{V}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=48\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=3\text{A}$	---	60	75	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=1.5\text{A}$	---	70	90	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=250\mu\text{A}$	1.2	1.8	2.5	V
			---	-5	---	$\text{mV}/^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_{\text{D}}=3\text{A}$	---	7	---	S

Dynamic and switching Characteristics

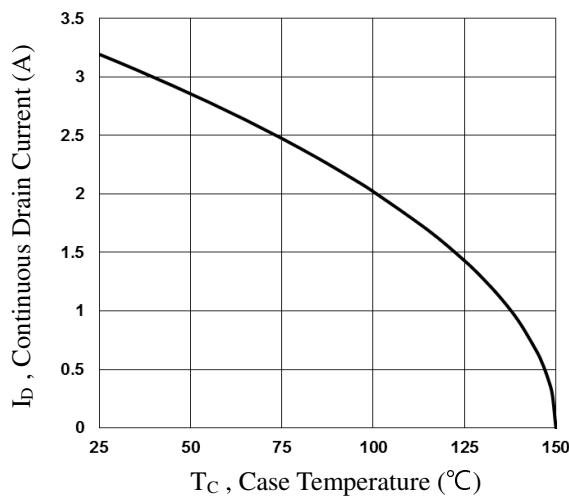
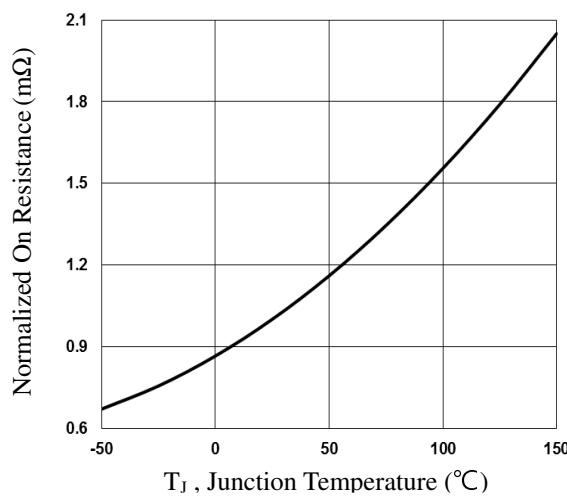
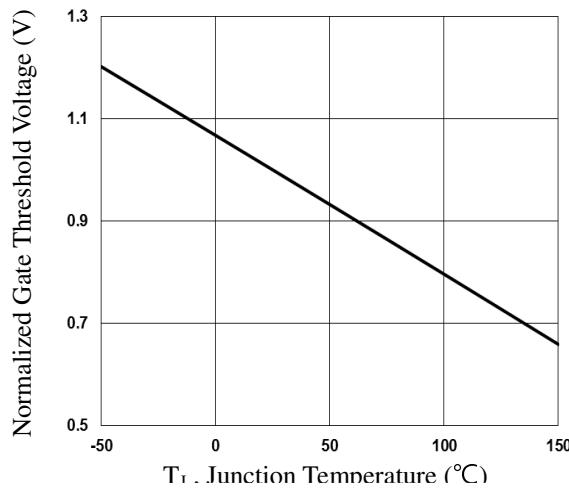
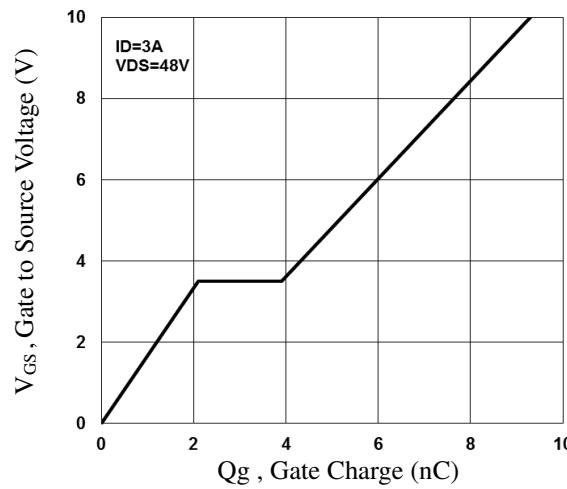
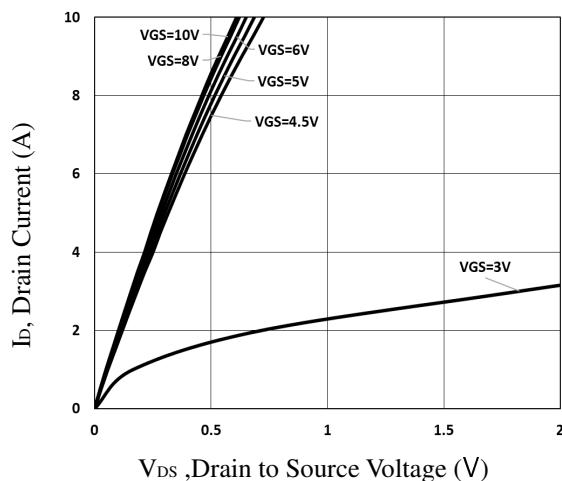
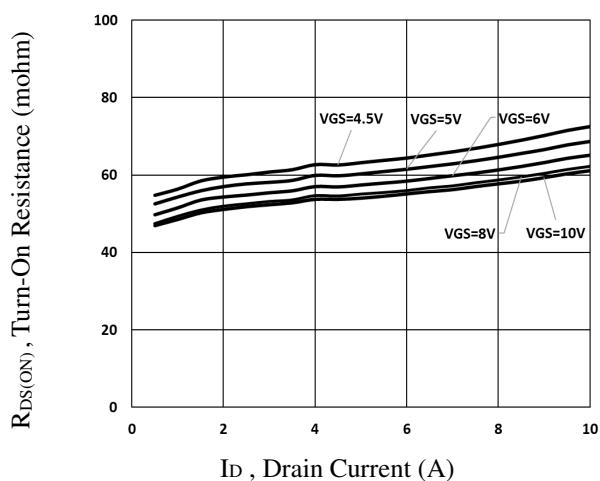
Q_g	Total Gate Charge ^{2, 3}	$V_{\text{DS}}=48\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=3\text{A}$	---	9.3	14	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	2.1	4	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	1.8	4	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{2, 3}	$V_{\text{DD}}=30\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_{\text{G}}=3.3\Omega$	---	2.9	6	ns
T_r	Rise Time ^{2, 3}		---	9.5	18	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{2, 3}		---	18.4	35	
T_f	Fall Time ^{2, 3}		---	5.3	10	
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	500	725	pF
C_{oss}	Output Capacitance		---	45	65	
C_{rss}	Reverse Transfer Capacitance		---	16	30	
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $F=1\text{MHz}$	---	2	4	Ω

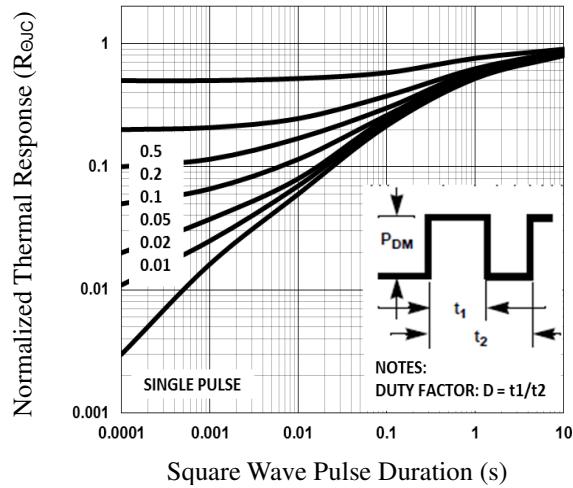
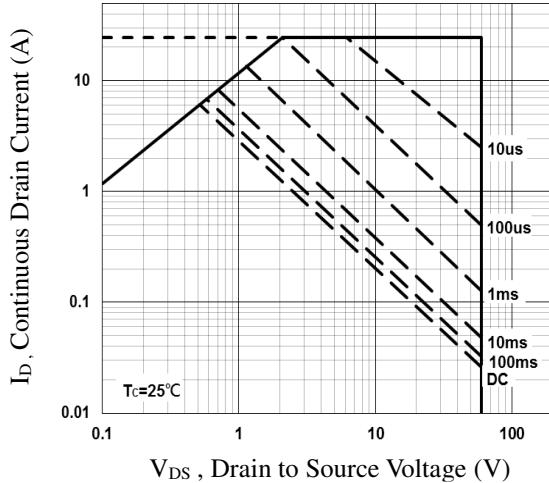
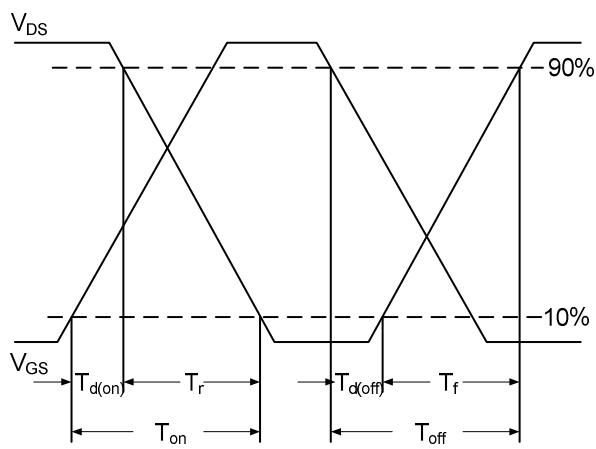
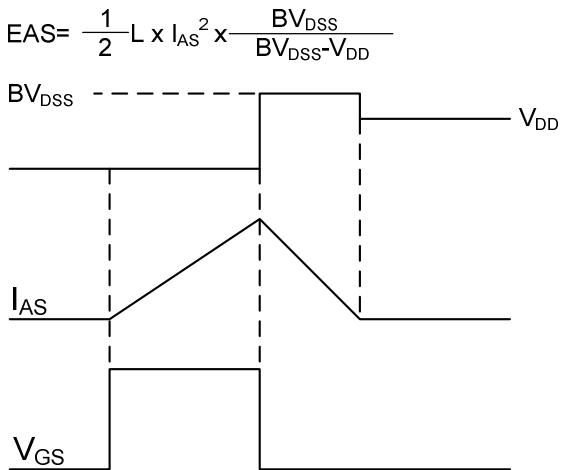
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	3.2	A
			---	---	6.4	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1	V
t_{rr}	Reverse Recovery Time ²	$V_{\text{GS}}=30\text{V}$, $I_s=1\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$	---	23.2	---	ns
Q_{rr}	Reverse Recovery Charge ²		---	14.3	---	nC

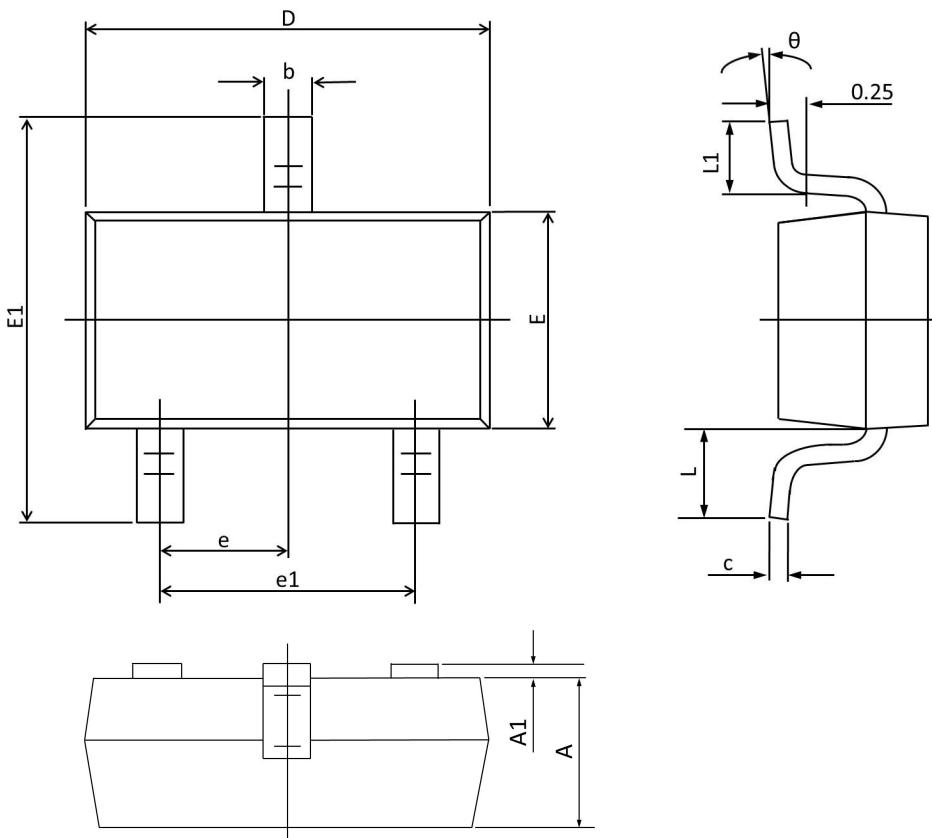
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_C

Fig.2 Normalized RD_{SON} vs. T_J

Fig.3 Normalized V_{th} vs. T_J

Fig.4 Gate Charge Waveform

Fig.5 Typical Output Characteristics

Fig.6 Turn-On Resistance vs. I_D


Fig.7 Normalized Transient Response

Fig.8 Maximum Safe Operation Area

Fig.9 Switching Time Waveform

Fig.10 EAS Waveform

SOT23-3S PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.000	0.035	0.039
A1	0.000	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.090	0.110	0.003	0.004
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
θ	1°	7°	1°	7°