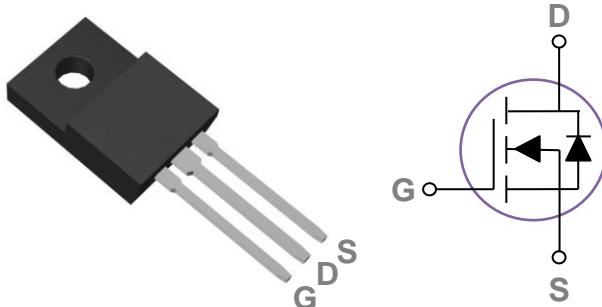


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### TO220F Pin Configuration



BVDSS	RDS(ON)	ID
150V	9.6mΩ	80A

### Features

- 150V, 80A, RDS(ON) = 9.6mΩ @ VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Networking
- Load Switch
- LED applications
- Quick Charger

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	150	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	80	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	51	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	320	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	392	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	28	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	144	W
	Power Dissipation – Derate above 25°C	1.15	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.87	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	150	---	---	V
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=120\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=120\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=85^\circ\text{C}$	---	---	10	$\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm100$	$\text{nA}$

**On Characteristics**

$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>3</sup>	$V_{\text{GS}}=10\text{V}$ , $I_D=25\text{A}$	---	8	9.6	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	2	3	4	V
$\text{gfs}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_D=3\text{A}$	---	13	---	S

**Dynamic and switching Characteristics**

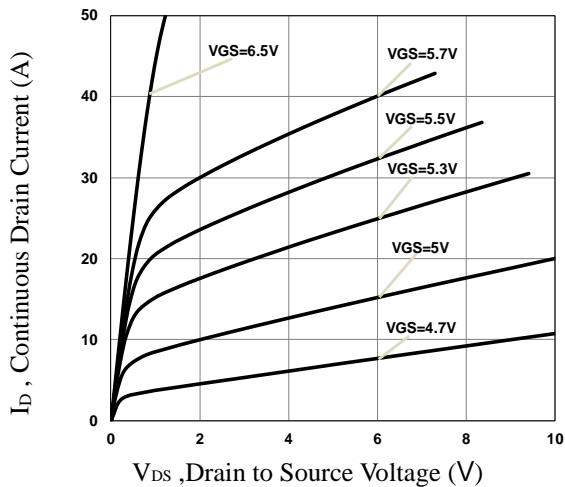
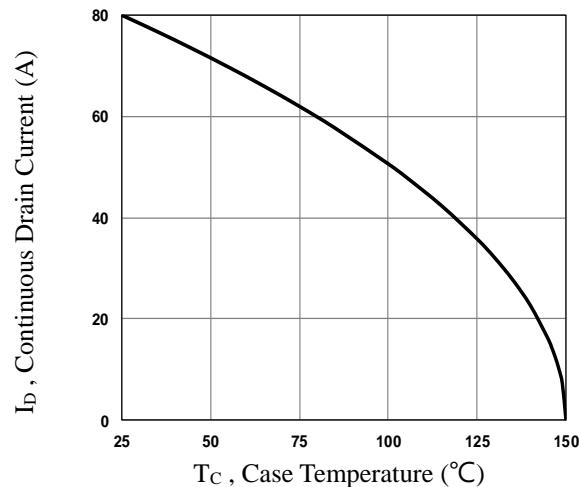
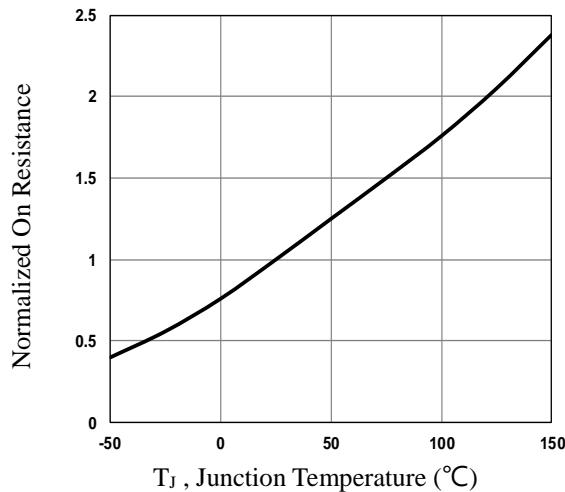
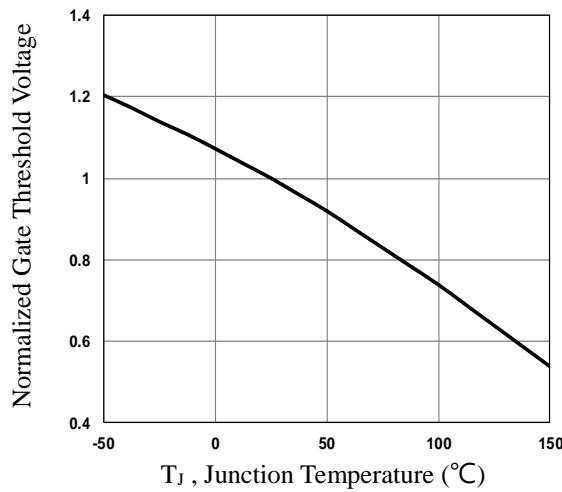
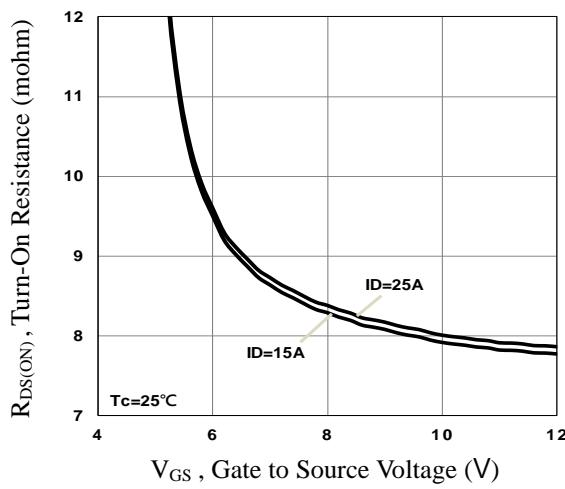
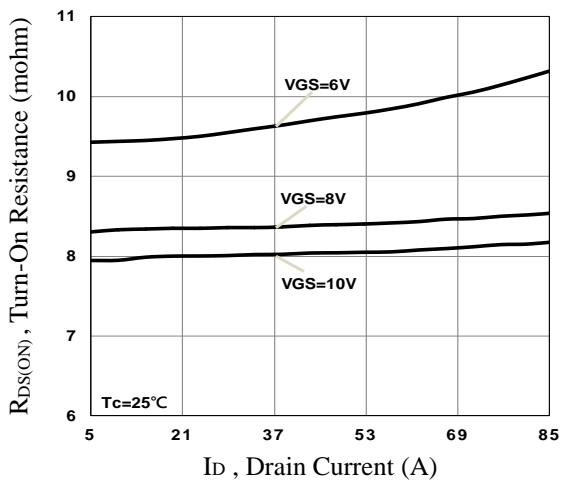
$Q_g$	Total Gate Charge <sup>3, 4</sup>	$V_{\text{DS}}=75\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=40\text{A}$	---	80	120	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>3, 4</sup>		---	18.5	28	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>3, 4</sup>		---	27	40	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>3, 4</sup>	$V_{\text{DD}}=75\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=6\Omega$ $I_D=40\text{A}$	---	18	32	ns
$T_r$	Rise Time <sup>3, 4</sup>		---	30	55	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>3, 4</sup>		---	25	45	
$T_f$	Fall Time <sup>3, 4</sup>		---	35	65	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=75\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	4600	6900	pF
$C_{\text{oss}}$	Output Capacitance		---	450	675	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	16	25	
$R_g$	Gate Resistance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=0\text{V}$ , $F=1\text{MHz}$	---	0.8	---	$\Omega$

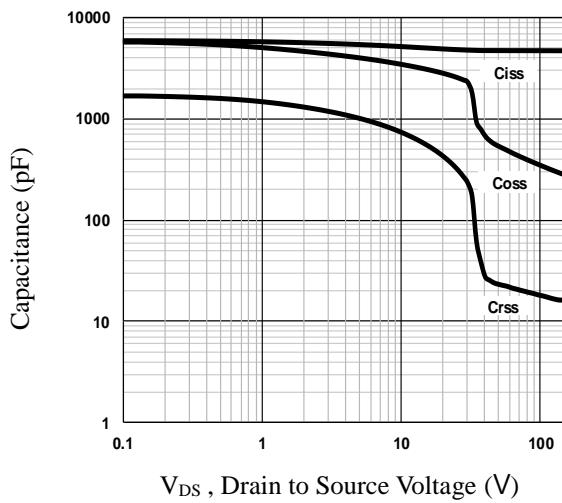
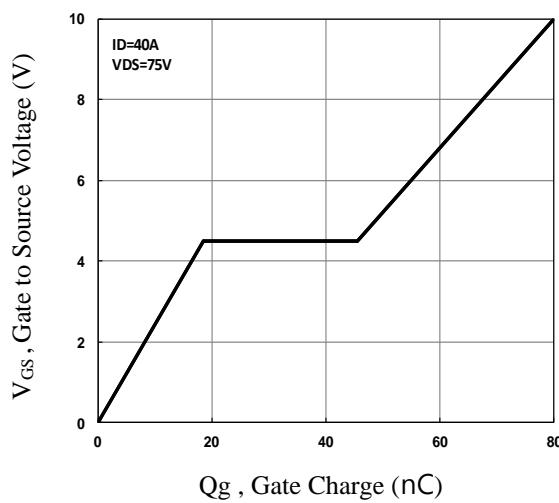
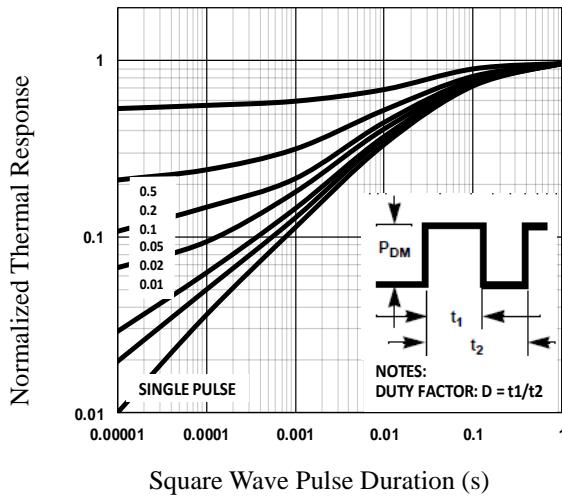
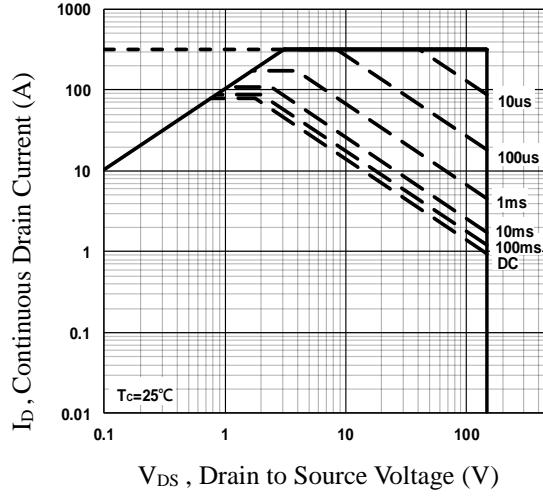
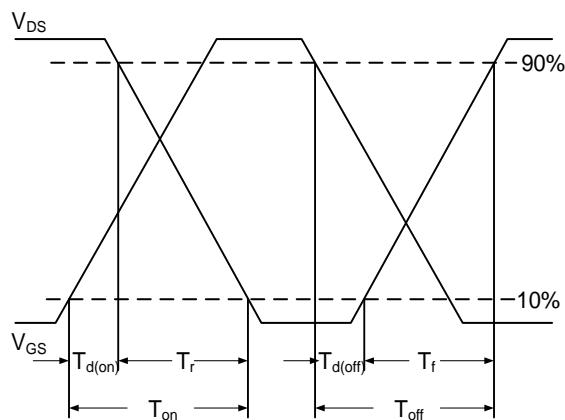
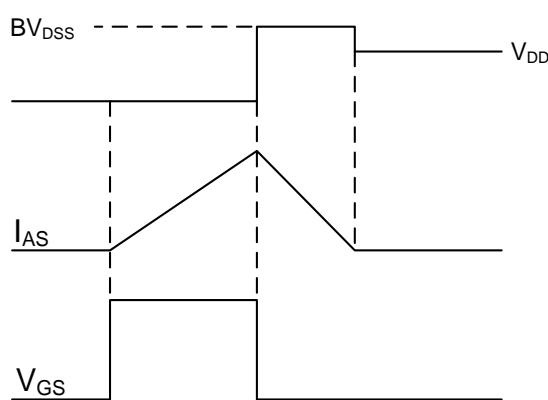
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	80	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	160	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V
$t_{\text{rr}}$	Reverse Recovery Time <sup>3</sup>	$V_R=100\text{V}$ , $I_s=10\text{A}$ $dI/dt=100\text{A}/\mu\text{s}$ , $T_J=25^\circ\text{C}$	---	105	---	ns
$Q_{\text{rr}}$	Reverse Recovery Charge <sup>3</sup>		---	400	---	nC

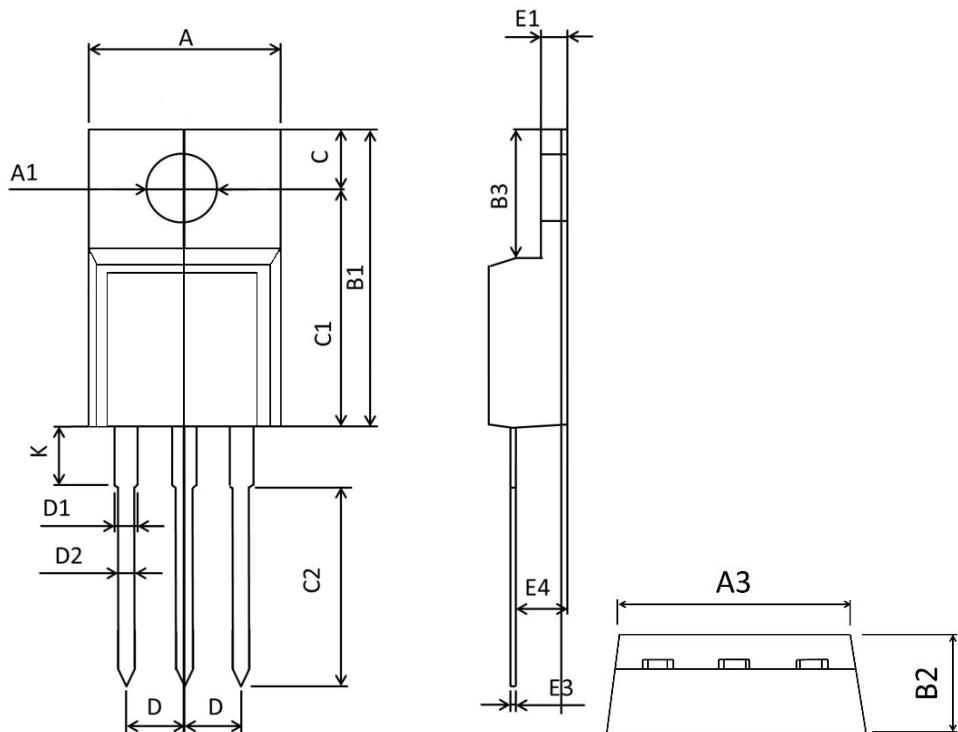
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{\text{DD}}=50\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=1\text{mH}$ ,  $I_{\text{AS}}=28\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .
3. The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.


**Fig.1 Typical Output Characteristics**

**Fig.2 Continuous Drain Current vs.  $T_c$** 

**Fig.3 Normalized  $R_{DS(on)}$  vs.  $T_j$** 

**Fig.4 Normalized  $V_{th}$  vs.  $T_j$** 

**Fig.5 Turn-On Resistance vs.  $V_{GS}$** 

**Fig.6 Turn-On Resistance vs.  $I_D$**


**Fig.7 Capacitance Characteristics**

**Fig.8 Gate Charge Characteristics**

**Fig.9 Normalized Transient Impedance**

**Fig.10 Maximum Safe Operation Area**

**Fig.11 Switching Time Waveform**

**Fig.12 EAS Waveform**

## TO220F PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	9.860	10.460	0.389	0.411
A1	3.100	3.500	0.122	0.138
B1	15.450	16.300	0.608	0.642
B2	4.400	5.000	0.173	0.197
B3	6.280	7.100	0.247	0.280
C	3.100	3.500	0.122	0.138
C1	12.270	12.870	0.483	0.507
C2	9.600	10.520	0.378	0.414
D	2.540BSC		0.1BSC	
D1	1.070	1.470	0.042	0.058
D2	0.600	1.000	0.024	0.039
K	2.800	3.500	0.110	0.138
E1	2.340	2.740	0.092	0.108
E3	0.350	0.650	0.014	0.026
E4	2.460	2.960	0.097	0.117