

### General Description

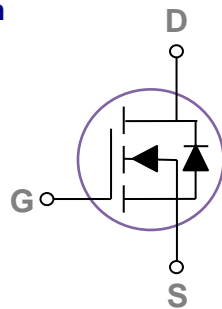
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
100V	6.5mΩ	100A

### Features

- 100V,100A,  $R_{DS(ON)} = 6.5m\Omega @ V_{GS} = 10V$
- Improved  $dv/dt$  capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### TO263 Pin Configuration



### Applications

- Networking
- Load Switch
- LED applications
- Quick Charger

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	+20/-12	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	100	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	63	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	400	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	320	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	80	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	184	W
	Power Dissipation – Derate above $25^\circ\text{C}$	1.47	W/ $^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-50 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-50 to 150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.68	$^\circ\text{C}/\text{W}$

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**  
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	100	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>J</sub> =85°C	---	---	10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =20V, V <sub>DS</sub> =0V	---	---	100	nA

**On Characteristics**

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	5.5	6.5	mΩ
		V <sub>GS</sub> =5V, I <sub>D</sub> =10A	'---	7	9	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.2	1.8	2.5	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =5A	---	8	---	S

**Dynamic and switching Characteristics**

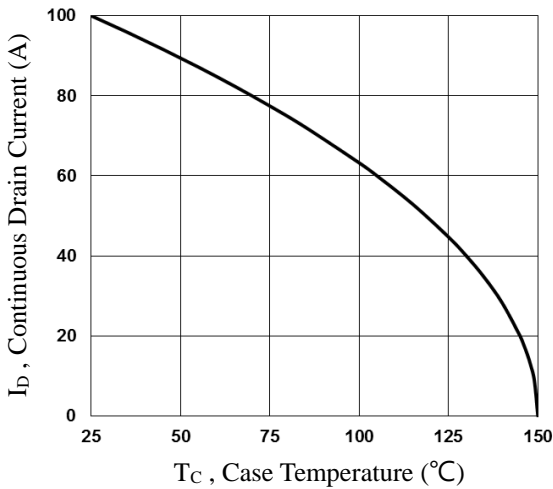
Q <sub>g</sub>	Total Gate Charge <sup>3, 4</sup>	V <sub>DS</sub> =80V, V <sub>GS</sub> =10V, I <sub>D</sub> =10A	---	58.2	100	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>3, 4</sup>		---	9.2	18	
Q <sub>gd</sub>	Gate-Drain Charge <sup>3, 4</sup>		---	20.8	30	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>3, 4</sup>	V <sub>DD</sub> =50V, V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω I <sub>D</sub> =1A	---	24	48	ns
T <sub>r</sub>	Rise Time <sup>3, 4</sup>		---	19.8	39	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>3, 4</sup>		---	46	92	
T <sub>f</sub>	Fall Time <sup>3, 4</sup>		---	26	52	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, F=1MHz	---	4570	9100	pF
C <sub>oss</sub>	Output Capacitance		---	1180	2300	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	49	98	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	---	2	4	Ω

**Drain-Source Diode Characteristics and Maximum Ratings**

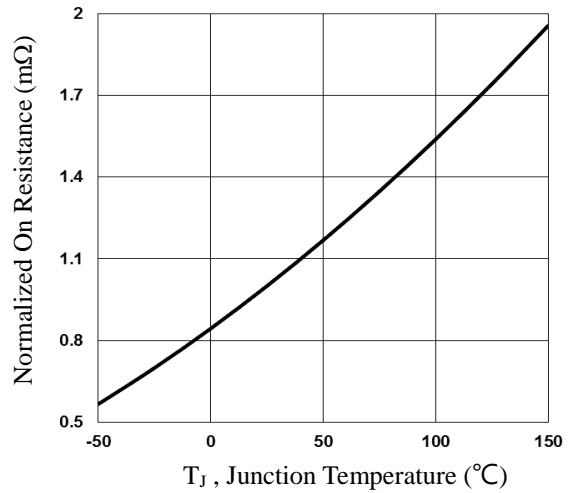
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	100	A
I <sub>SM</sub>	Pulsed Source Current		---	---	200	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1	V

Note :

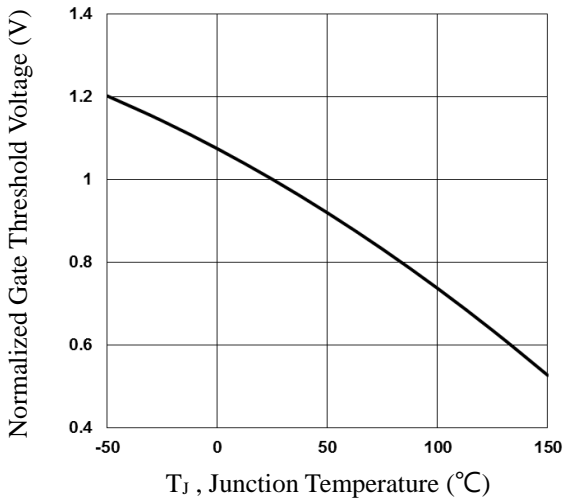
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=80A., R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.



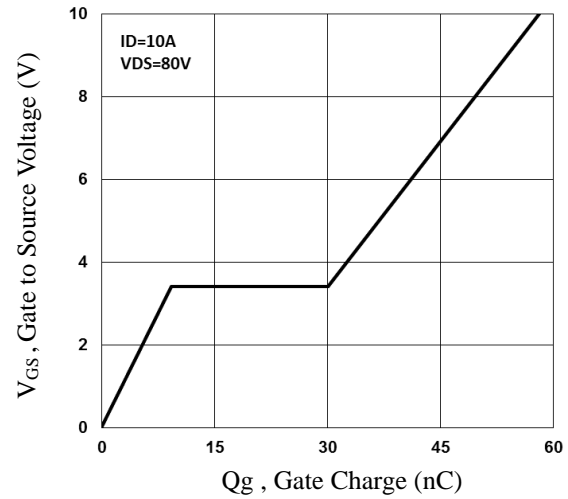
**Fig.1 Continuous Drain Current vs. T<sub>c</sub>**



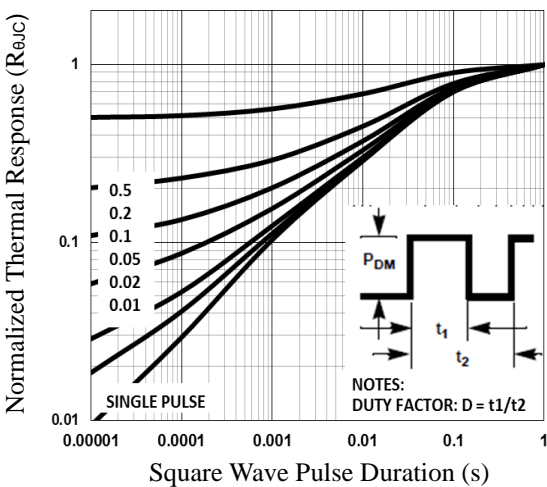
**Fig.2 Normalized RD<sub>SON</sub> vs. T<sub>j</sub>**



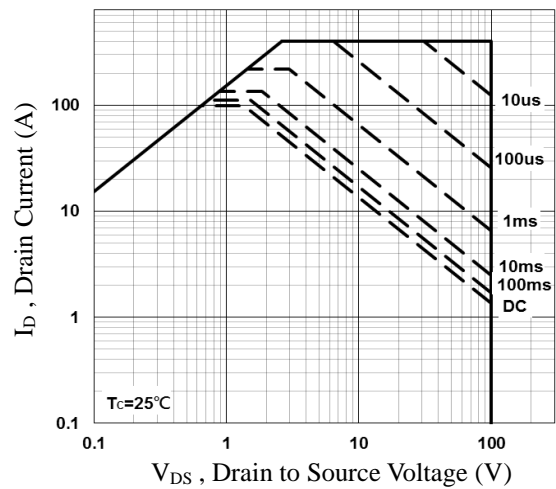
**Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>**



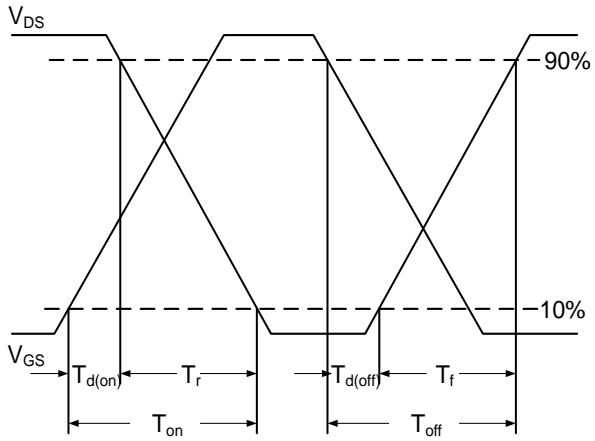
**Fig.4 Gate Charge Characteristics**



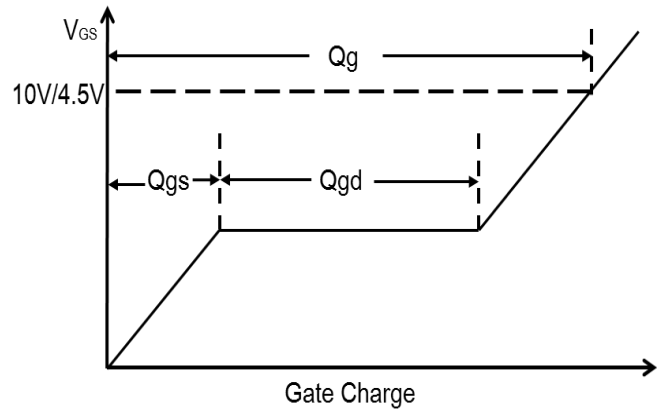
**Fig.5 Normalized Transient Impedance**



**Fig.6 Maximum Safe Operation Area**



**Fig.7 Switching Time Waveform**



**Fig.8 Gate Charge Waveform**

TO263 PACKAGE INFORMATION

