

General Description

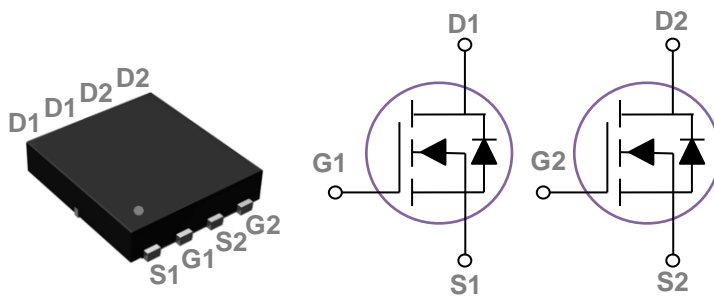
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
40V	9mΩ	30A

Features

- 40V,30A, $R_{DS(ON)} = 9m\Omega @ V_{GS} = 10V$
- AEC-Q101 qualified
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

PPAK5x6 Dual Pin Configuration



Applications

- MB / VGA / Vcore
- POL Applications
- SMPS 2nd SR
- USB Type C

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_c=25^\circ\text{C}$)	30	A
	Drain Current – Continuous ($T_c=100^\circ\text{C}$)	19	A
	Drain Current – Continuous ($T_A=25^\circ\text{C}$)	11	A
	Drain Current – Continuous ($T_A=70^\circ\text{C}$)	8.8	A
I_{DM}	Drain Current – Pulsed ¹	120	A
EAS	Single Pulse Avalanche Energy ²	64	mJ
IAS	Single Pulse Avalanche Current ²	36	A
P_D	Power Dissipation ($T_c=25^\circ\text{C}$)	46	W
	Power Dissipation – Derate above 25°C	0.37	W/ $^\circ\text{C}$
	Power Dissipation ($T_A=25^\circ\text{C}$)	2	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	2.7	$^\circ\text{C}/\text{W}$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)
Static State Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	40	---	---	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =40V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =32V, V _{GS} =0V, T _J =125°C	---	---	10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
R _{DS(ON)}	Static Drain-Source On-Resistance ³	V _{GS} =10V, I _D =8A	---	7.2	9	mΩ
		V _{GS} =4.5V, I _D =4A	---	9.5	12	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1	1.6	2.5	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =10A	---	13	---	S

Dynamic Characteristics

Q _g	Total Gate Charge ^{3,4}	V _{DS} =20V, V _{GS} =4.5V, I _D =8A	---	12.2	24	nC
Q _{gs}	Gate-Source Charge ^{3,4}		---	3.3	7	
Q _{gd}	Gate-Drain Charge ^{3,4}		---	6.7	13	
T _{d(on)}	Turn-On Delay Time ^{3,4}	V _{DD} =15V, V _{GS} =10V, R _G =3.3Ω I _D =1A	---	13.2	25	ns
T _r	Rise Time ^{3,4}		---	2.2	5	
T _{d(off)}	Turn-Off Delay Time ^{3,4}		---	72	130	
T _f	Fall Time ^{3,4}		---	4.5	10	
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, F=1MHz	---	1220	2200	pF
C _{oss}	Output Capacitance		---	130	250	
C _{rss}	Reverse Transfer Capacitance		---	55	110	
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	---	2.2	---	Ω

Guaranteed Avalanche Energy

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy	V _{DD} =25V, L=0.1mH, I _{AS} =6A	1.8	---	---	mJ

Drain-Source Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V, Force Current	---	---	30	A
I _{SM}	Pulsed Source Current ³		---	---	60	A
V _{SD}	Diode Forward Voltage ³	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=36A., R_G=25Ω, Starting T_J=25°C.
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.

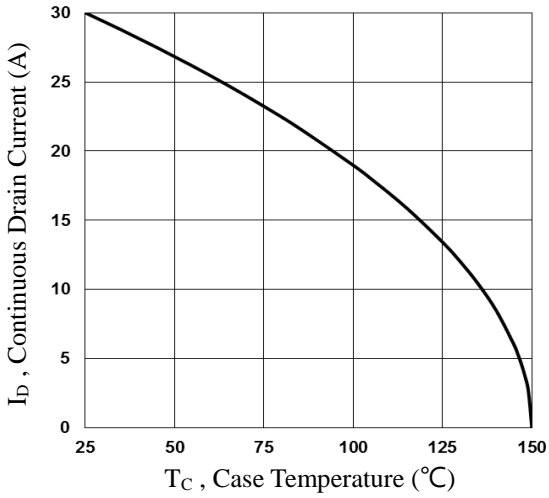


Fig.1 Continuous Drain Current vs. T_c

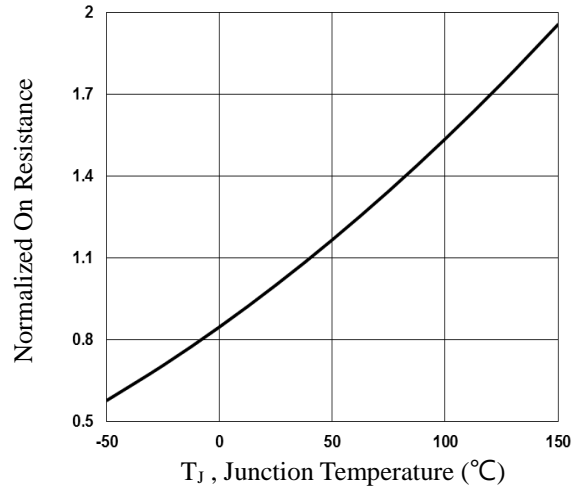


Fig.2 Normalized $R_{DS(on)}$ vs. T_j

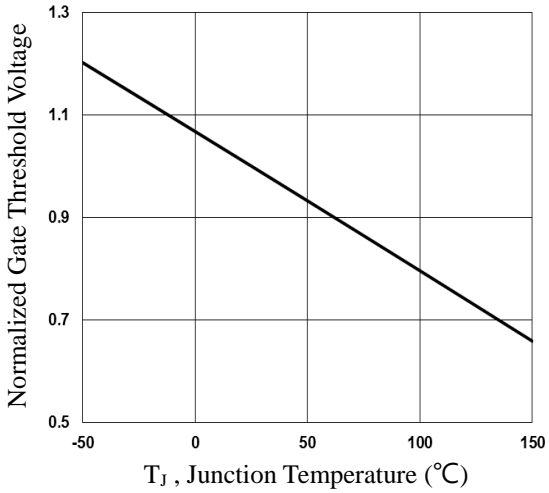


Fig.3 Normalized V_{th} vs. T_j

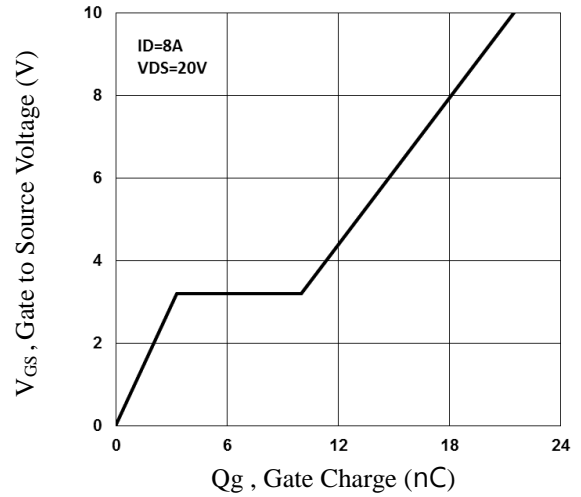


Fig.4 Gate Charge Waveform

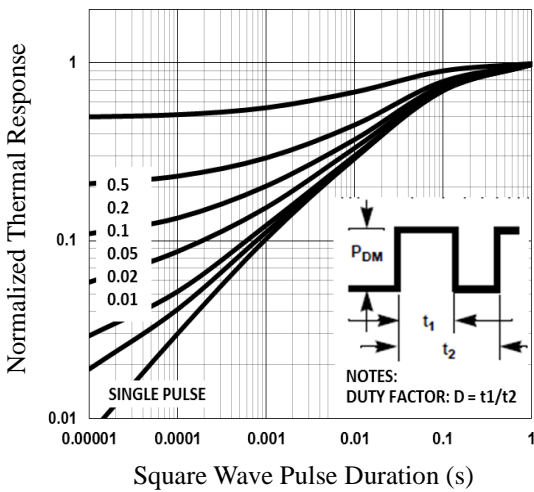


Fig.5 Normalized Transient Impedance

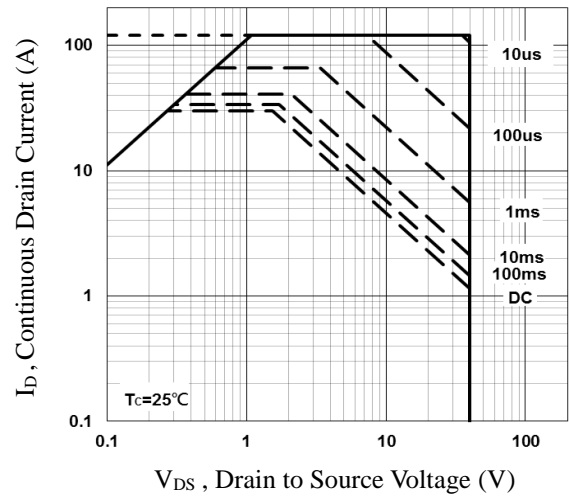


Fig.6 Maximum Safe Operation Area

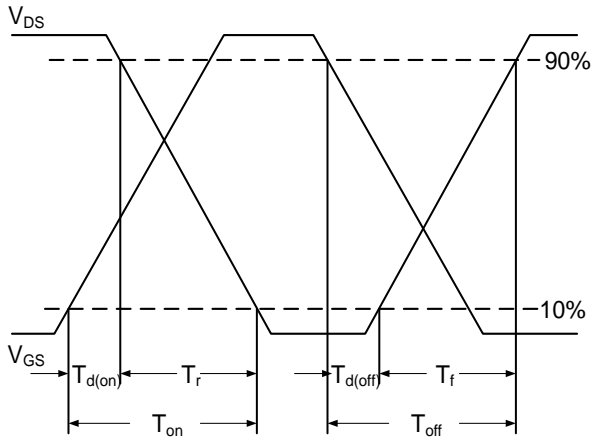


Fig.7 Switching Time Waveform

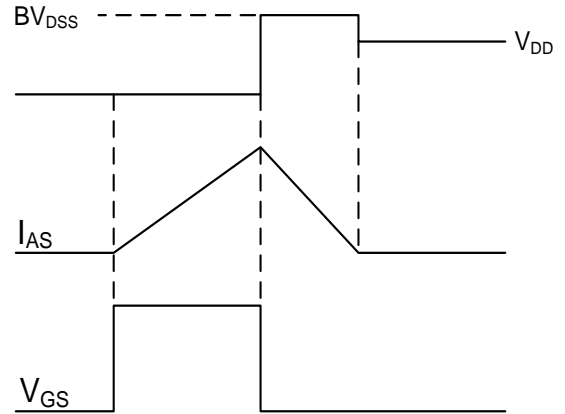
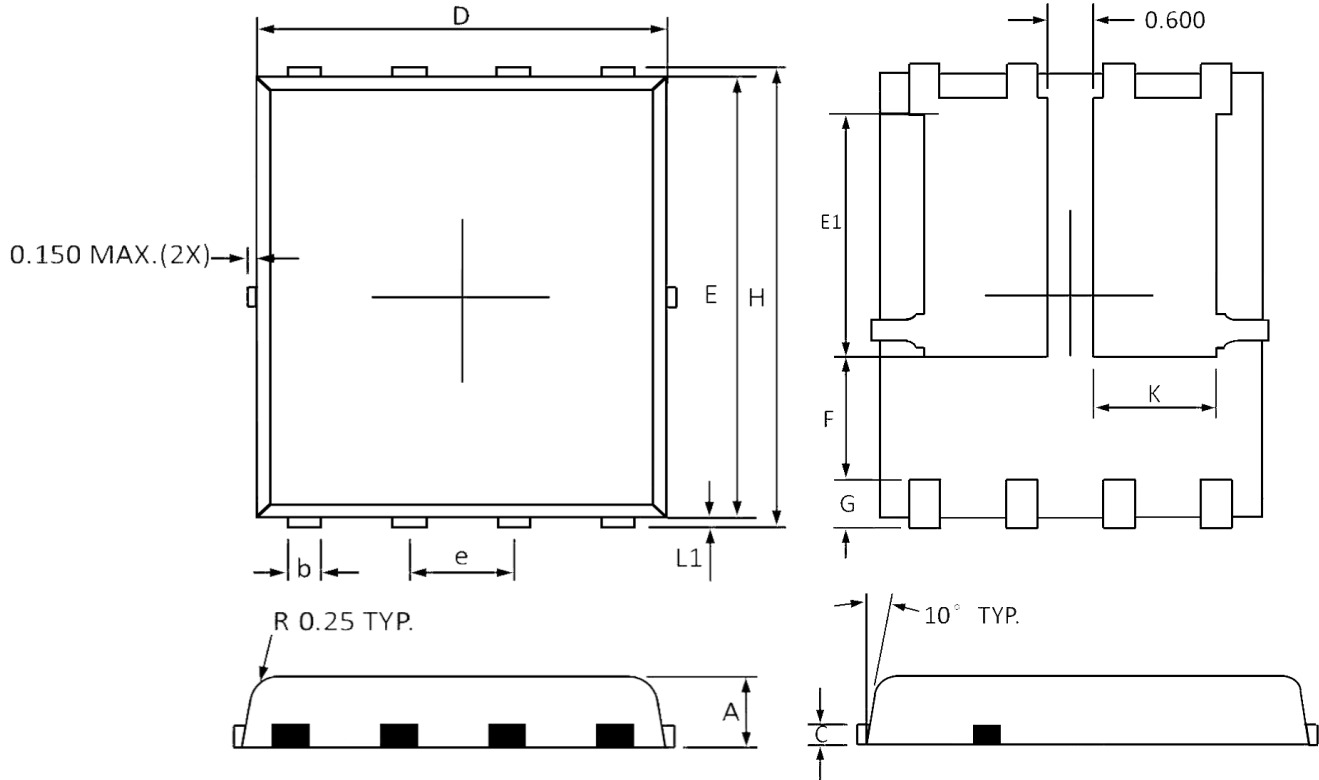


Fig.8 EAS Waveform

PPAK5x6 Dual PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.800	1.200	0.031	0.047
b	0.300	0.510	0.012	0.020
C	0.250 Ref		0.010 Ref	
D	4.800	5.400	0.189	0.213
E	5.450	5.960	0.215	0.235
E1	3.200	3.800	0.126	0.150
e	1.27 BSC		0.050 BSC	
F	1.000	1.900	0.039	0.075
G	0.380	0.800	0.015	0.031
H	5.850	6.300	0.230	0.248
L1	0.050	0.250	0.002	0.010
K	1.500	1.900	0.059	0.074

PPAK5X6 Dual RECOMMENDED LAND PATTERN

