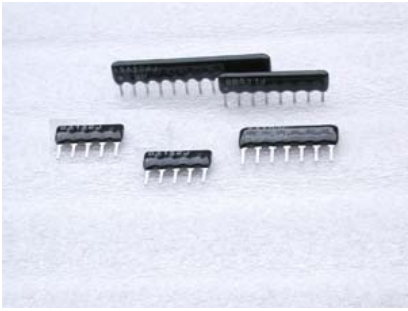
RESISTOR NETWORK—SIP RNL SERIES



Features

- High reliability with RUO2 paste
- Miniature, high density packaging
- Combinations of different ohmic values are available

Explanation of Part Number & Ordering Procedure:

R	N	L	A	1	0	G	0	1	0	3	B	0	0

Resistor Type:
Please fill "RNL" to indicate "Resistor Network Low Profile".

Circuit Type:
A=Single common
B=Isolated

No. of Pins:
04 = 4 pins
05 = 5 pins
06 = 6 pins
07 = 7 pins
08 = 8 pins
09 = 9 pins
10 = 10 pins
11 = 11 pins
12 = 12 pins
13 = 13 pins
14 = 14 pins

Tolerance:
G = ±2%, J = ±5%

Resistance Value:
E-24 series:
The 1st digit will be "0"; the 2nd & 3rd digits are for the significant figures of the resistance and the 4th digit denotes number of zeros following.

Packing Type:
B =Bulk in cassette

Packing Qty:
Please indicate "0" for Bulk/Box packing.

Special Feature:
0 = NIL

Note:
Ordering procedure (Example: RNL A type 10PIN 2% 10KΩ B/B)

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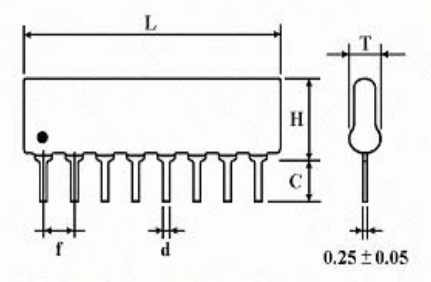
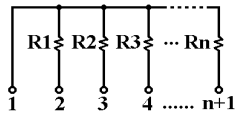
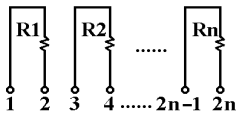
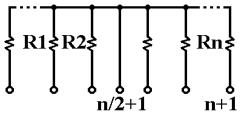
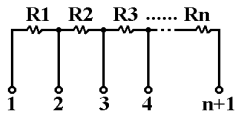
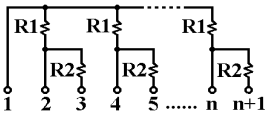
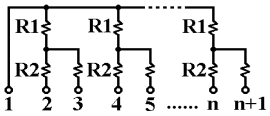
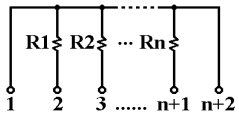
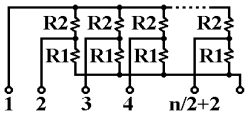
Dimension (mm)

Type	L (Max.)	H (Max.)	T (Max.)	C +0.3 -0.2	d ± 0.1	f ± 0.2
4pin	10.2	5.08	2.5	3.2	0.5	2.54
5pin	12.7					
6pin	15.3					
7pin	17.8					
8pin	20.4					
9pin	22.9					
10pin	25.4					
11pin	28.0					
12pin	30.5					
13pin	33.1					
14pin	35.6					



Rating

Power Rating at 70 °C	Max. Working Voltage	Max. Over-load Voltage	Dielectric With-standing Voltage	Resistance Range	Resistance Tolerance	Operating Temp. Range
B Type: 0.2W	100V	150V	200V	R Type: 100Ω~10KΩ	±1% ±2% ±5%	-55°C ~+125°C
Others: 0.125W				Others: 10Ω ~ 1MΩ		

A	B	C	D
			
R1=R2=.....=Rn	R1=R2=.....=Rn	R1=R2=.....=Rn	R1=R2=.....=Rn
E	**P**	**T**	**R**
			
R1=R2 or R1≠R2	R1=R2 or R1≠R2	R1=R2=.....=Rn	R1=R2 or R1≠R2

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Performance Specifications:

Characteristics	Test Methods	Limits
Temperature coefficient JIS – C – 5202 5.2	-55 °C ~ +125 °C	±200PPM / °C for 50Ω ~ 1MΩ ±250PPM / °C for < 50Ω or > 1MΩ
Short – time overload JIS – C – 5202 5.5	Rated Voltage x 2.5 for 5 seconds.	± (0.5% + 0.1Ω)
Insulation resistance JIS – C – 5202 5.6	100V DC for 1 min.	10,000 Mega ohm Min.
Dielectric withstanding voltage JIS – C – 5202 5.7	Resistors shall be clamped in the trough of a 90 ° metallic V– block and shall be tested at AC potential respectively specified in the above list for 60 + 10 / -0 seconds.	No evidence of flashover mechanical damage, arcing or insulation break down.
Terminal Strength JIS – C – 5202 6.1	Tensile: 1Kg, 30 seconds. Bending: 500g., 2 Times.	± (0.5% + 0.1 Ω)
Resistance to Soldering Heat JIS – C – 5202 6.4	350 °C ± 10°C , for 3 seconds.	± (0.5% + 0.1 Ω)
Solderability JIS – C – 5202 6.5	235ºC ± 5ºC, 3 seconds.	Covering 95%
Thermal Shock JIS – C – 5202 7.3	Load V, Room Temp., 30 min. Unload, -55°C, 15 min. Over 2 hrs. in room temp. before measuring.	± (0.5% + 0.1 Ω)
Temperature cycling JIS – C – 5202 7.4	Resistance change after continuous five cycles for duty cycle specified below: Step 1: -55ºC±3ºC, 30 mins. Step 2: Room temp., 10 – 15 mins. Step 3: +125 ºC±2 ºC, 30 mins. Step 4: Room temp., 10 – 15 mins.	± (0.5% + 0.1 Ω)
Load life in humidity JIS – C – 5202 7.9	40ºC, 90 - 95% RH Rated Voltage for 1,000 hrs. (1.5 hours “on”, 0.5 hour “off”)	± (3.0% + 0.1Ω)
Load Life JIS – C – 5202 7.10	70ºC at Rated Voltage for 1,000 hrs. (1.5 hours “on”, 0.5 hour “off”)	± (3.0% + 0.1Ω)